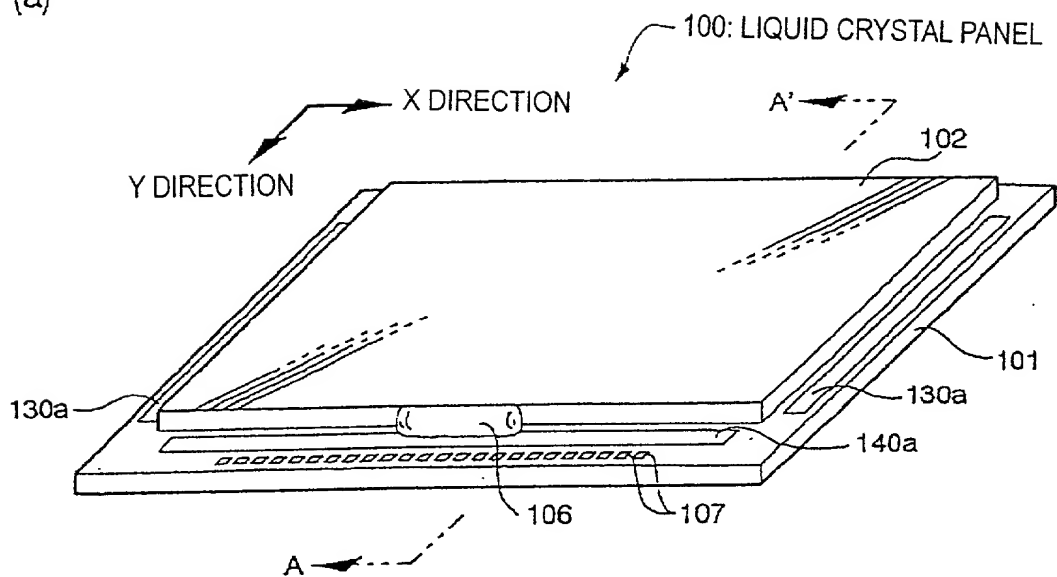
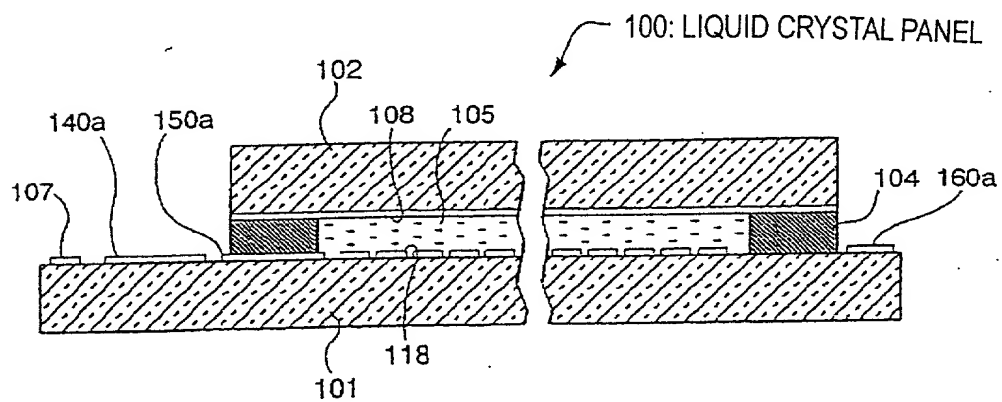


FIG. 1

(a)



(b)



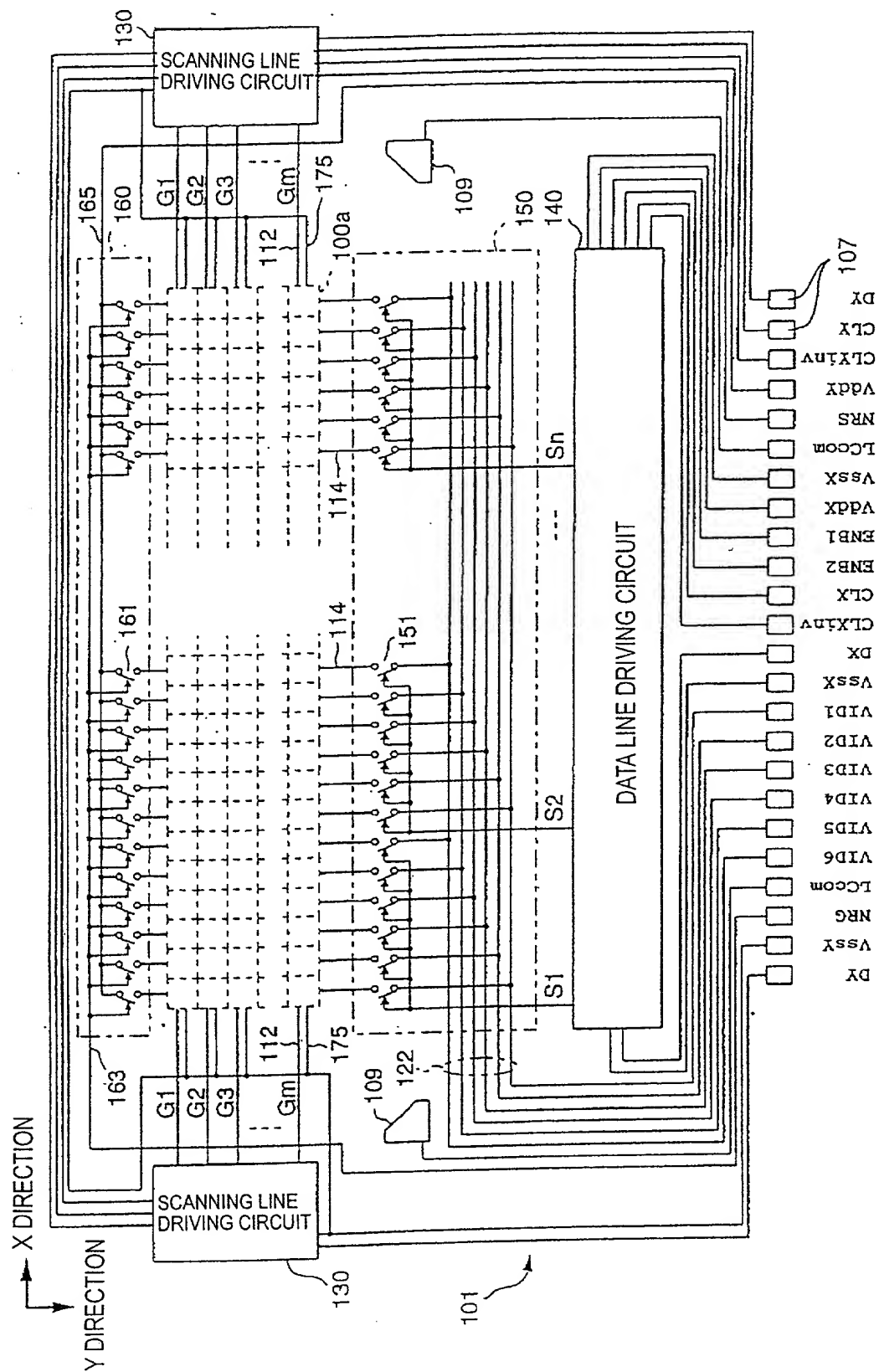
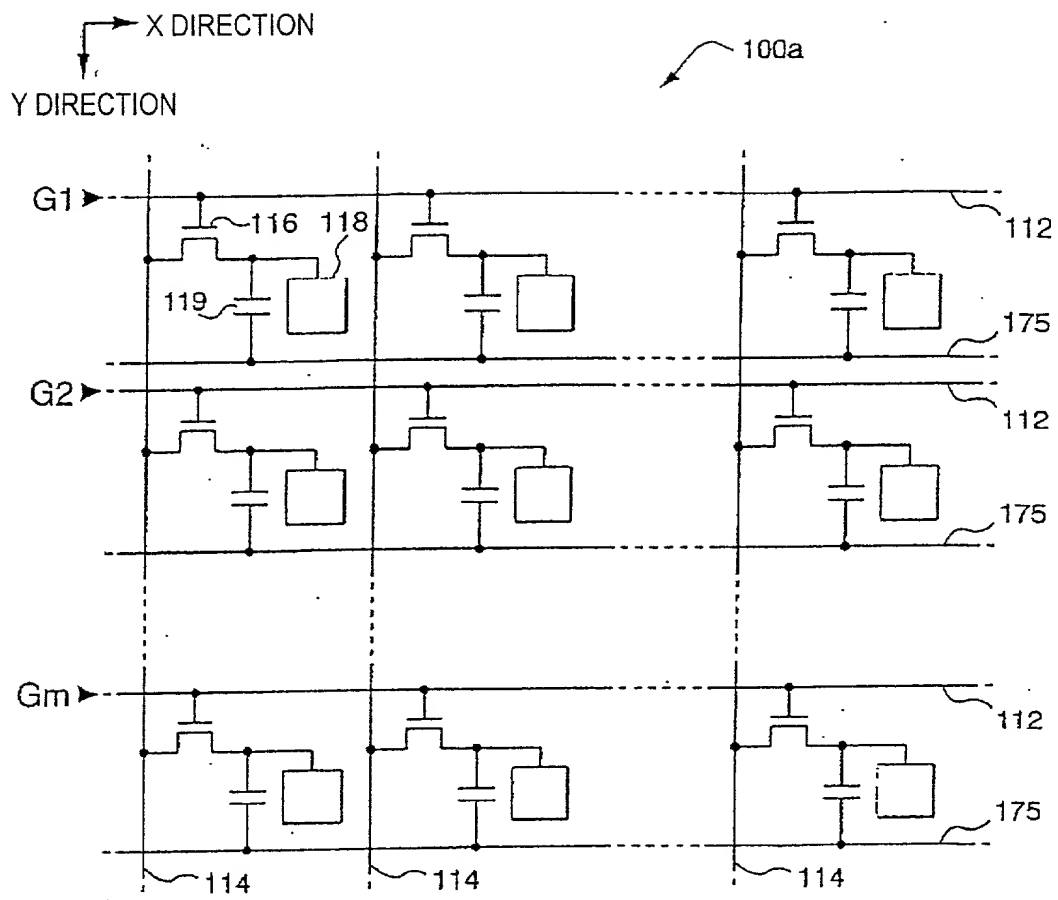


FIG. 3



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FIG. 4

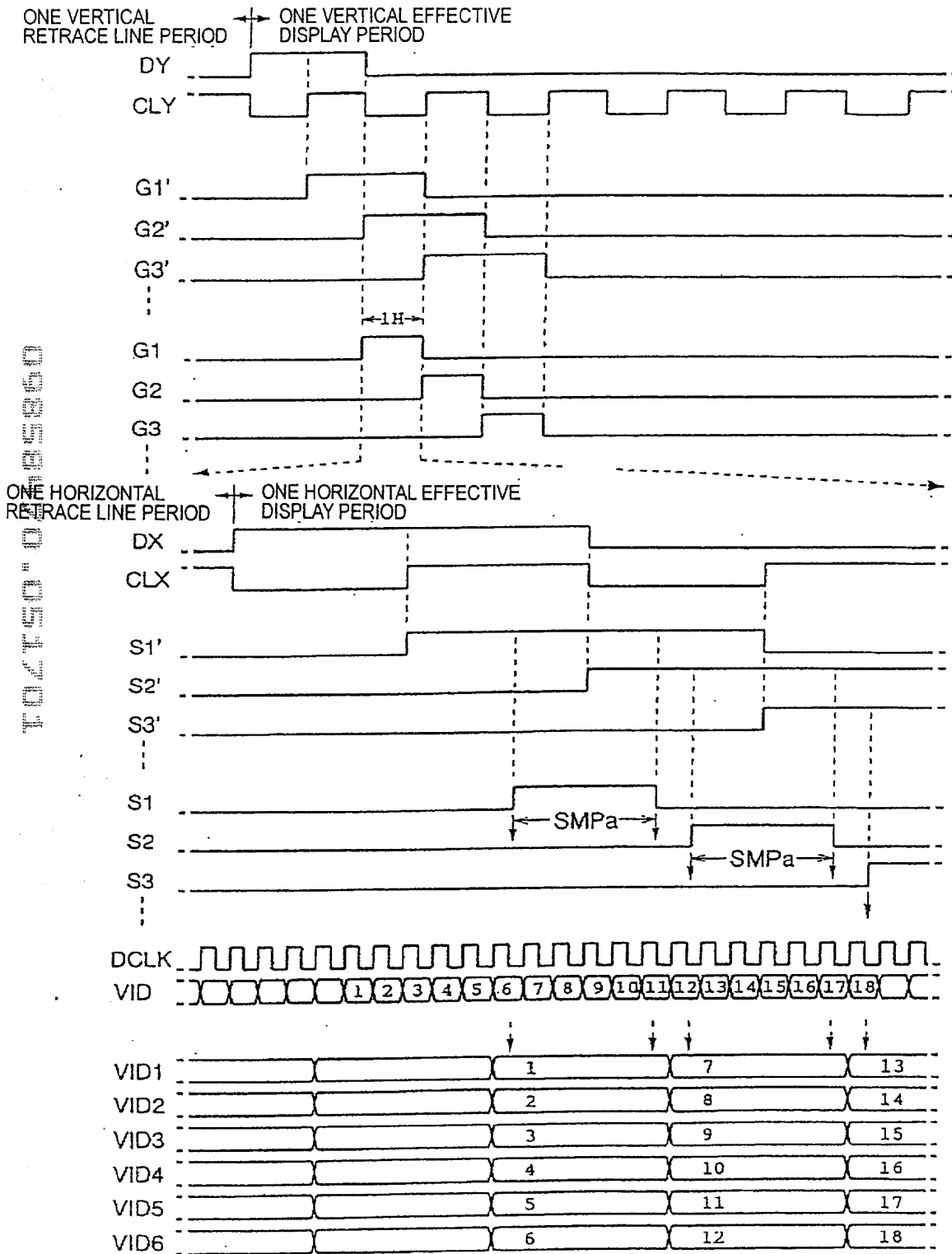
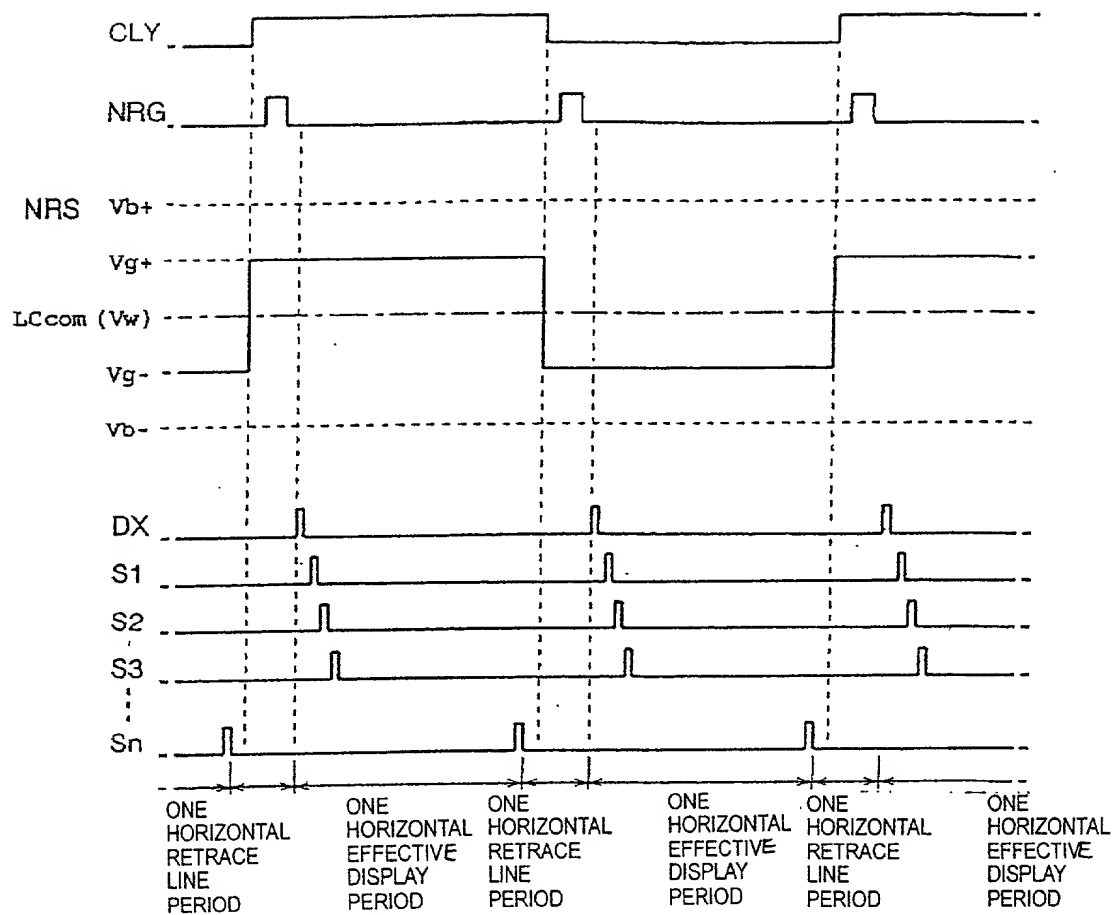


FIG. 5



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DECEMBER

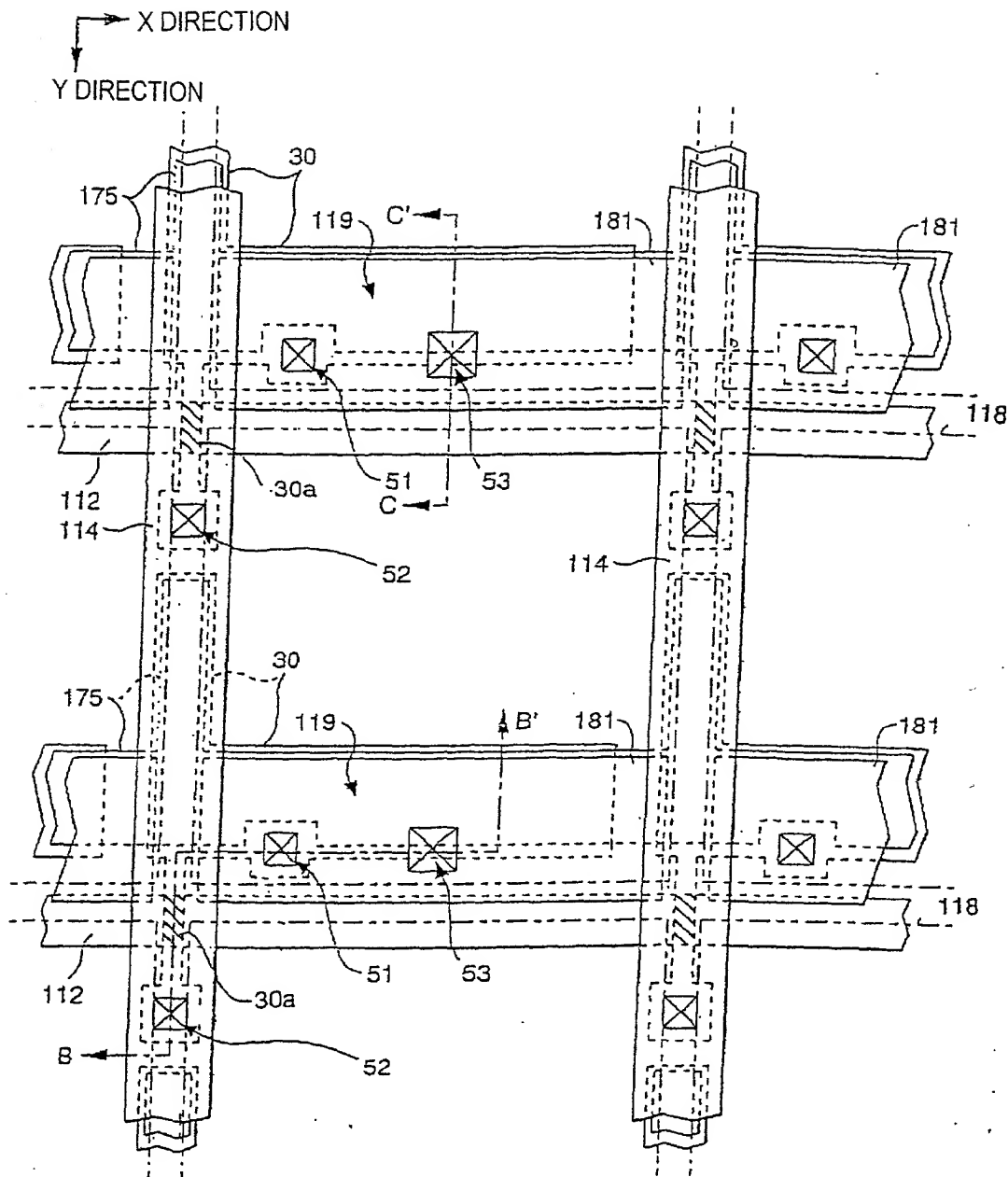
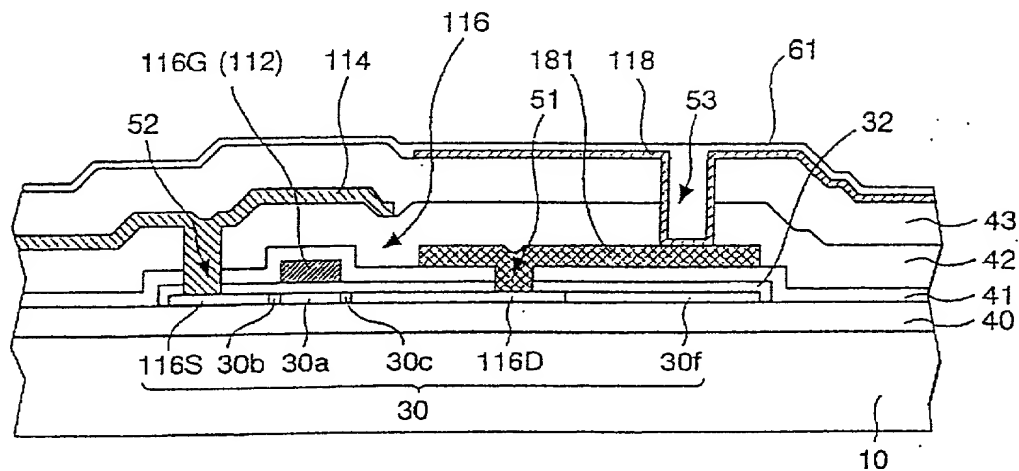
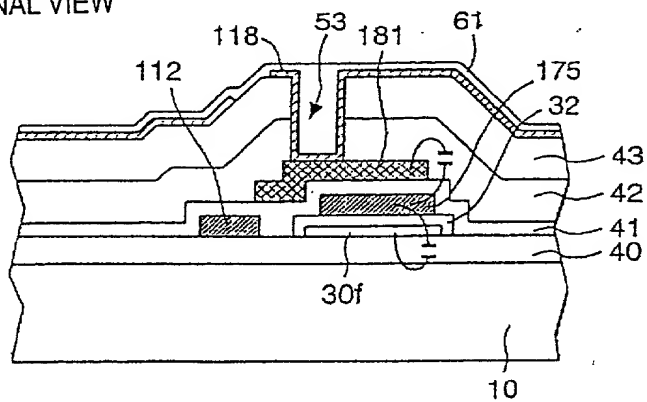


FIG. 7

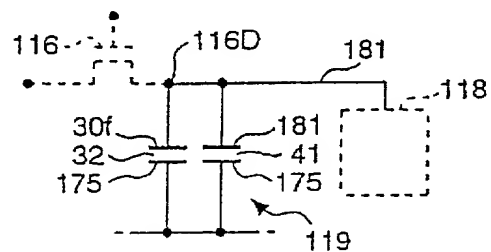
(a) LINE B-B' CROSS-SECTIONAL VIEW



(b) LINE C-C' CROSS-SECTIONAL VIEW



(c) EQUIVALENT CIRCUIT OF STORAGE CAPACITOR



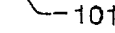
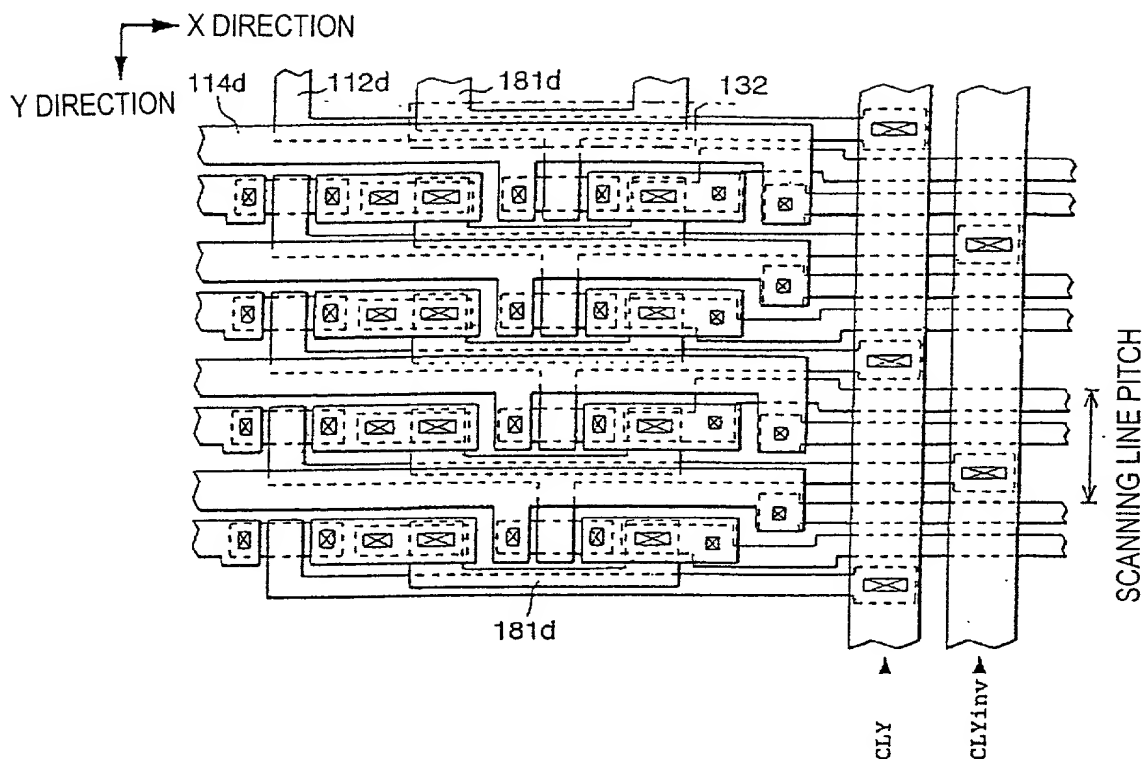
[illegible]

FIG. 9

(a)



(b)

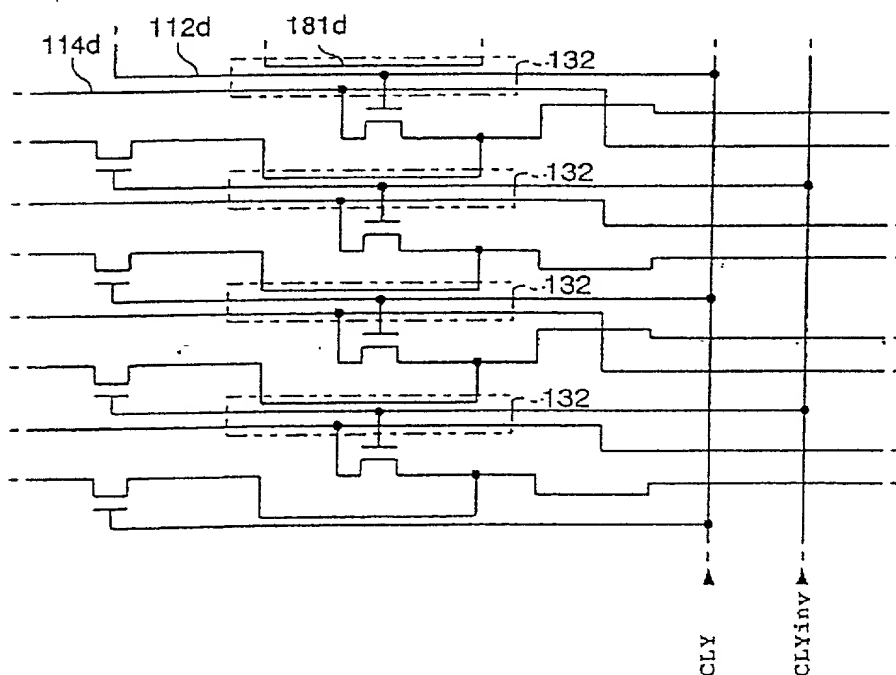
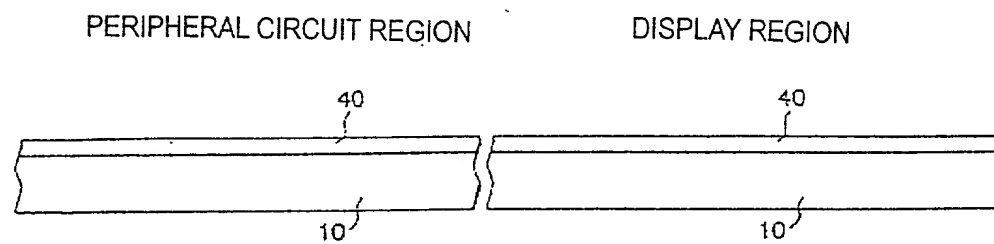
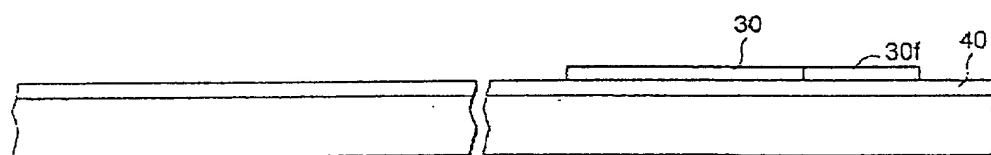


FIG. 10 |

(1)



(2)



(3)



FIG. 11

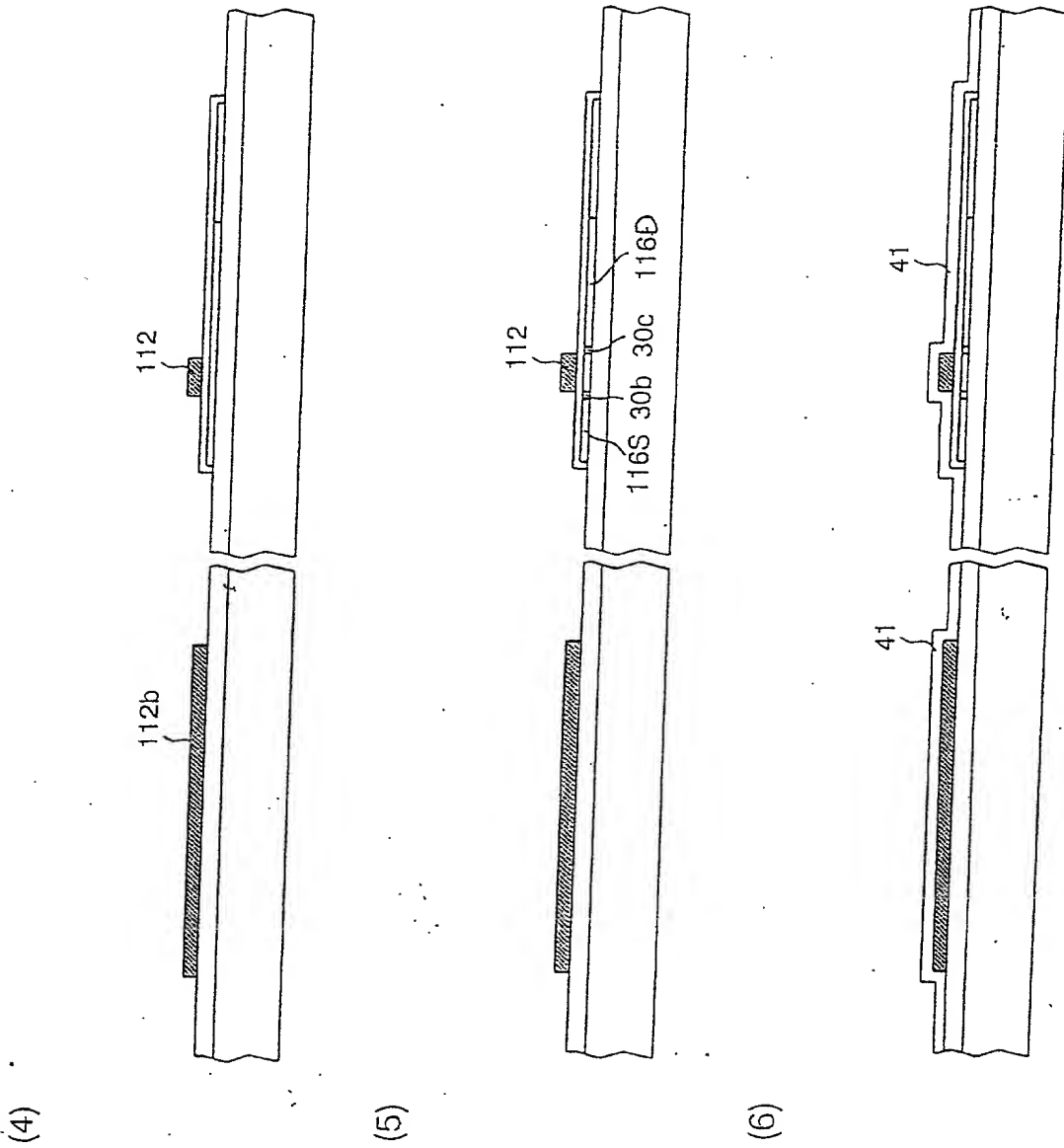
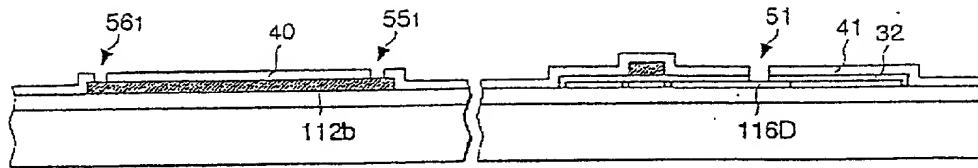
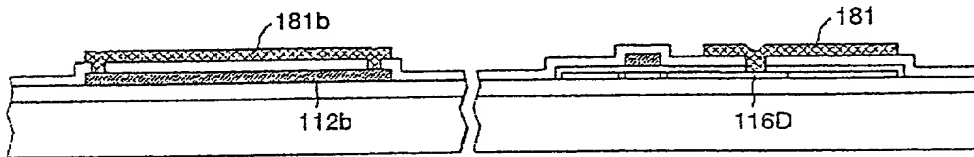


FIG. 12

(7)



(8)



(9)

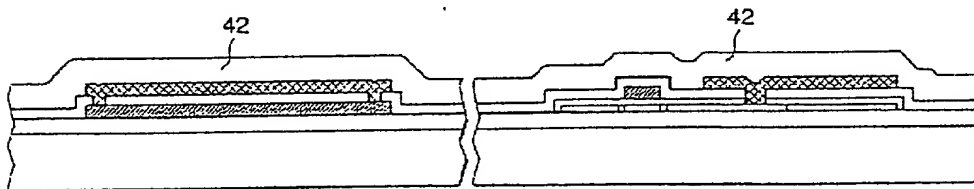
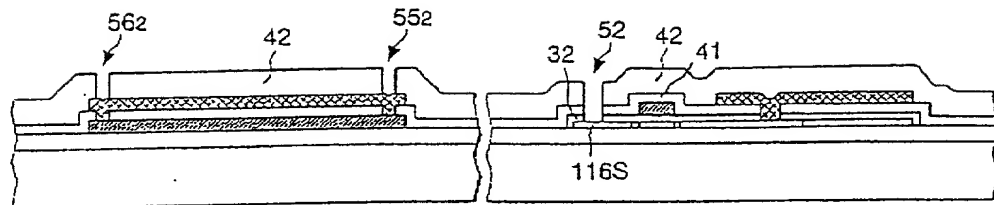
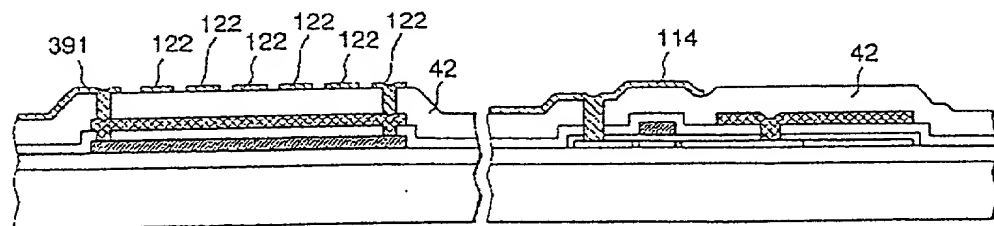


FIG. 13

(10)



(11)



(12)

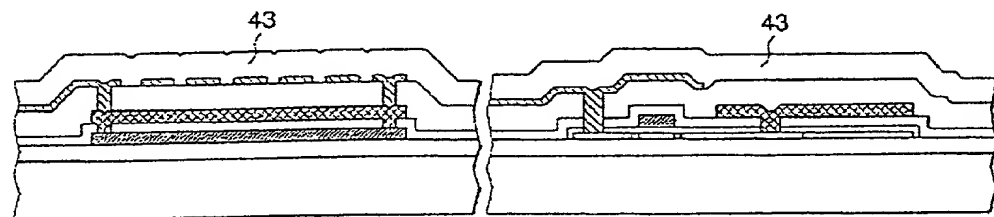
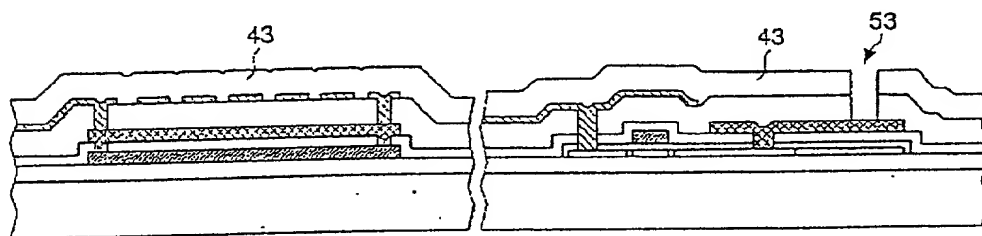


FIG. 14

(13)



(14)

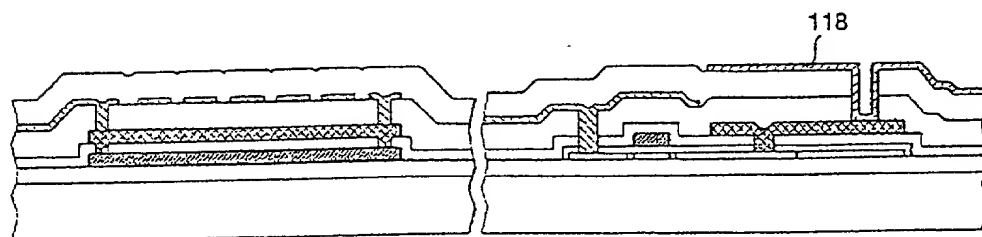
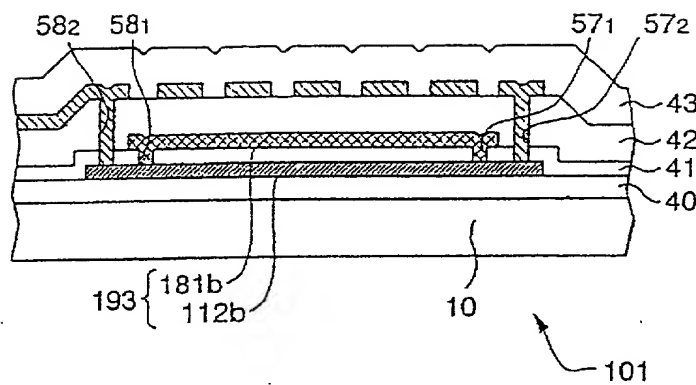


FIG. 15'

(a)



(b)

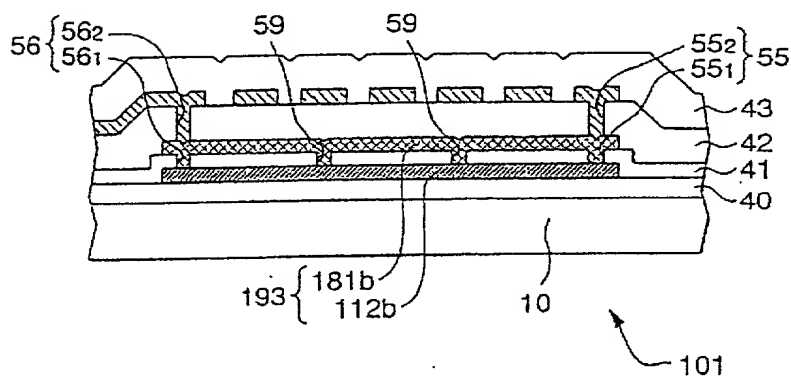


FIG. 16

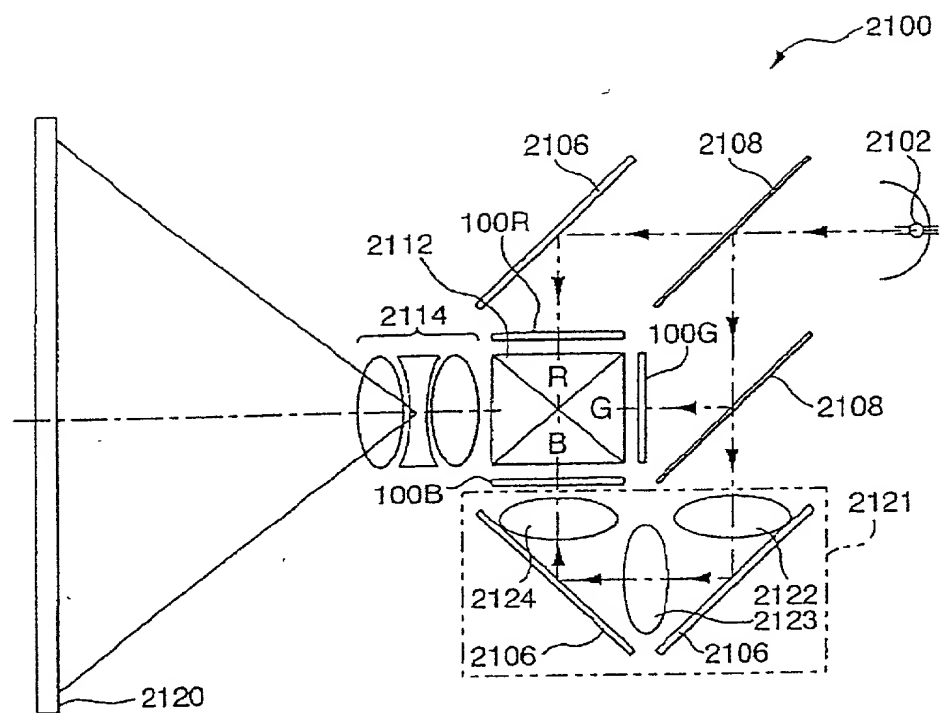


FIG. 17

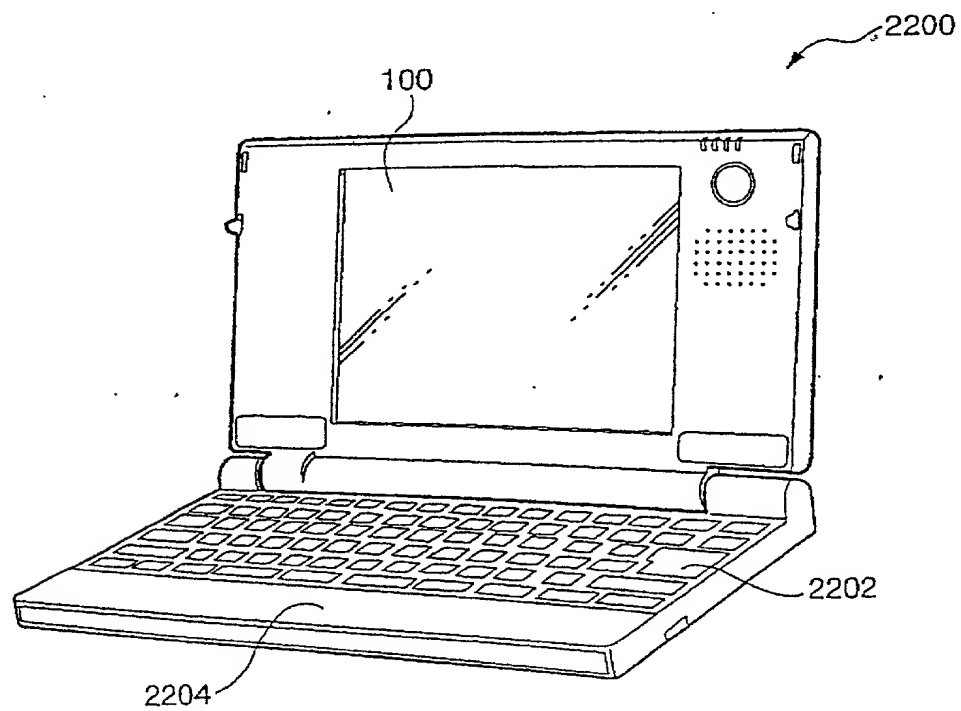
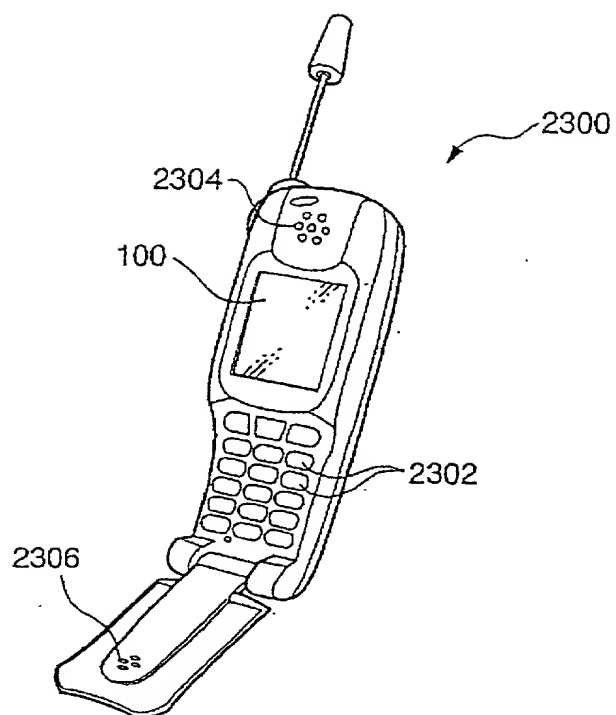


FIG. 18



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